COPY OF PAPERS ORIGINALLY FILED

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

\mathbf{M}^{μ}	
Application Serial No	. 09/851.634
Filling Date	May 8, 200
Inventor Alan	R. Reinberg
Assignee Micron Tec	hnology. Inc
Group Art Unit	. Unknow
Examiner	. Unknowr
Attorney's Docket No	
Title: Microelectronic Device Fabricating Method, Integrated Circuit, and Intermediate Construction	d

INFORMATION DISCLOSURE STATEMENT

References -- See Attached Form PTO-1449

In compliance with 37 C.F.R. §§ 1.56, 1.97 and 1.98, your attention is directed to the United States patents and other references listed on the attached Form PTO-1449. No admission is made regarding whether all the submitted references are prior art.

The listed references were cited by, or submitted to, the Office in the parent, co-pending application of the above-identified application. The above-identified application is a divisional application of co-pending application Serial No. 09/561,794, filed May 1, 2000. Such prior disclosure is sufficient for the above-identified application as far as copies of the references are concerned. 37 C.F.R. § 1.98(d) and MPEP § 609(2).

Citation of these references is respectfully requested.

Respectfully submitted,

Dated: 31 Dec 2001 By: James E. Lake
Reg. No. 44,854